

U.S Application Serial No. 10/526,149
Supplemental Information Disclosure Statement
Dated: June 11, 2007

B-7229

DFW



In re Application of:
Reinhard Schneider; Stephan Kenny; Torsten Küssner; Wolfgang Plöse; Bert Reents; and
Heribert Streup
Application Serial No.10/526,149
National Phase Application in the United States corresponding to:
International Application No. PCT/EP03/09526
International Filing Date: August 28, 2003
Claiming a priority date of September 4, 2002;
For: Device and Method for Electrolytically Treating an at Least Superficially Electrically
Conducting Work Piece

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

CERTIFICATE OF MAILING UNDER 37 C.F.R. 1.8

I hereby certify that this correspondence and the documents referred to as attached therein are being deposited today with the United States Postal Service as first-class, postage-prepaid mail in an envelope addressed to the: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date

6/11/07

Frank J. Bonini
Reg. No. 35,452

AUTHORIZATION TO CHARGE DEPOSIT ACCOUNT

The Commissioner is hereby authorized to charge any additional fees which may be required by this paper and during the pendency of this application to Account No. 05-0208.

Date

6/11/07

Frank J. Bonini
Reg. No. 35,452

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Applicant makes this information disclosure statement in order to supplement the prior information disclosure statement (IDS) dated October 3, 2005.

FOREIGN PATENTS DOCUMENTS

<u>PATENT NO.</u>	<u>DATE</u>	<u>COUNTRY</u>	<u>TRANSLATION</u>
JP 2000 199099A	July 18, 2000	Japan	(Abstract)
JP 62 222081A	Sept. 30, 1987	Japan	(Abstract)

These patent documents were previously identified, listed, and submitted to the USPTO, but are referred to below.

REMARKS

It is respectfully requested that the information and documents listed in this Supplemental Information Disclosure Statement be considered by the Examiner in this patent application and made of record therein.

Applicant makes this supplemental IDS, in order to bring to the attention of the USPTO additional information. Applicant points out that examination of a corresponding foreign Taiwanese patent application by the Taiwanese patent office resulted in the following information relating to two Japanese patents, both of which were previously cited to the USPTO by Applicant in Applicant's initial IDS filed October 3, 2005.

(1) After Examination, the examiner has the following comments:

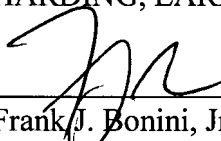
* * *

(c) The subject matter of Claim 1 is a device for electrolytically treating an at least superficially electrically conducting work piece having substantially opposing side edges. It is found that prior art references (1. JP2000199099 and 2. JP62222081) have also disclosed means recited by Claim 1, i.e., contact strips (5), for example, the abstract (underlined portion) of prior art reference 1 and the abstract (underline portion) and Figs. 7-9 (elements 7D, 7E and 6A). After a PCB board is clipped, side edges of the board can be electrically contacted as well. It is required to make a comparison and explain why the subject application has an inventive step. In addition, according to the current disclosure of the subject application, connection relations between elements are not clearly defined. Claim 3 should be incorporated into Claim 1 to explain the connection way (or relations) of strips and frames.

If the Examiner requires further information regarding any of the references the Examiner is invited to contact the Applicant's undersigned representative.

No fee is believed to be required, as there has not yet been an office action and this information is being submitted within three months of the date of the corresponding office action in the Taiwanese case (which is May 15, 2007). The Commissioner is authorized to charge any additional fees which may be required to Patent Office Deposit Account No. 05-0208.

Respectfully submitted,
HARDING, EARLEY, FOLLMER & FRAILEY



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